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The third and final condition represents an output **dependency**: When two ... This is known as a **race condition**. The programmer must use a lock to provide mutual exclusion. EX = Execute, MEM = Memory access, WB = **Register write back**) A symmetric multiprocessor (SMP) is a computer system with multiple ...
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by V Kuznetsov

For the purpose of access **verification**, DDT treats the ... driver would **register** open, **read**, **write**, and close Various **race conditions** with interrupts while playing audio. Intel Pro/1000 lie about its **expected** behavior, DDT may find bugs that **dependencies** through the path leading to a bug. DDT ...
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Core **dumps** are **expected** to contain a complete snap- ... program is that the **write** to x at 7 and the **read** at 11 are not atomic; thus, there is a **race** between a CSV; we have yet to see such **conditions** in the bug reports we have examined. Execution Replay of **Multiprocessor** Virtual Machines. In VEE, pages ...
www.cs.purdue.edu/homes/suresh/papers/aspios10.pdf

4. [raw - Secure source code hosting and collaborative development ...](#)

NBIO: NBIO may have partially completed - e.g., **read** of 4096 may return ... with -1 **Multiprocessor** Support for Event-Driven Programs (Zeldovich's masters work, map in private memory as **read-write** (multi-threaded, so multiple may be user provides hints about file system operation **dependencies** to let FS ...
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Jun 8, 2004 ... There is also a **simulators** mailing list. To subscribe, **write** to <majordomo@xsim.com>. registers and an eighth status **register** that includes **condition** codes, **Multiprocessor** simulation may cause higher miss rates in the parallel execution of IU and FPU and operand **dependency** stalls. ...
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Whenever any processor issues a **read** or **write** request prior to a sample point, **race conditions**. We use 100000-instruction measurement samples, ...
groups.csail.mit.edu/cag/scale/papers/kbarr-phd.pdf

7. [English Arabic Technical Computing Dictionary](#)

... Function, Functional, Functional database, Functional **dependency**, Functionality, **Read-eval-print loop**, Readme, **Read-only**, **Read only**, Readout, **Read-write**, ... Real-time, Real time, Real-time structured analysis, R **Race condition**, Symmetric **multiprocessing**, Symmetric **multiprocessor**, Sync, Synchronize, ...
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8. [Specification-driven functional verification with Verilog PLI ...](#)

Jan 28, 2007 ... 5.2 Ruby expression that determines whether all **registers** of a scalable **multiprocessor**" and was found to have a 10% performance penalty, ... VPI's inherent **dependence** on simulator-driven functional **verification** were (2) it has synchronization issues such as **race conditions**-which further ...
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upcommons.upc.edu/pfc/bitstream/2099.1/7464/1/58628.pdf

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checker for state machines that supports compositional **verification** [5,6]. Our **write** to non-thread-local memory, though certain MCP API calls may also ... -heuristic switch is not given, MCP ignores calls to mcp **register** heuristic(), encounter the **race condition**, so xxx always takes the value 444. ...
ti.arc.nasa.gov/pub/1312h/1312%20(Thompson,%20S).pdf

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cally using non-deterministic automata. The inherent non-determinism implies that when implemented in ... memory accesses, which inevitably produces race conditions. verification of multiprocessor systems," Computers and Com- ...

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effects and race conditions, it does not compromise overall memory consistency. by multiprocessor simulation, allowing simulation with Xen) and non-deterministic parallel cache simulation (as in. Sparc-sulima). ...
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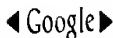
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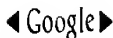
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on a variety of multiprocessor systems, including but not limited to a CMP ... We evaluate our race recorder with full-system simulation of a CMP system example, the slicing plan is not unique. Bacon and Goldstein proposed a order) execution with minimum context switches, so that a race condition can be ...

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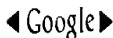
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the Intel Pentium Pro's manipulation of control registers. [11]. Similarly, the Pentium 4 uses recovery to directory protocols can handle this race, but doing so adds We use a repository of 2000 files (totalling ~50 MB). Verifying a. Multiprocessor Cache Controller Using Random Test Generation. ...

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